A diagram of a diagram

Description automatically generatedDaisy Waveforms

A graph with red lines

Description automatically generatedA black and white pattern

Description automatically generatedA black and white grid

Description automatically generatedFor br2=1:  
1. AND gate sees /BR and /BBSY during first time interval and takes one time delay to assert BG1

2. /BR1 takes one time delay after BG1 to stop asserting

A graph with red lines

Description automatically generated3. /BBSY also takes one time delay after BG1 to assert

4. /BR remains asserted because /BR3 is still active

A graph with blue lines

Description automatically generated5. BG1 goes low one time delay after /BBSY stops asserting (one delay through AND gate)

6. Since there’s still a request, BG1 goes high one time delay after /BBSY has stopped asserting

7. Device 1 doesn’t want the signal, so BG2 goes high after one delay

8. Device 2 doesn’t want the signal, so BG3 goes high after one delay

9. /BR3 takes one time delay after BG3 to stop asserting. Since /BR3 is directly connected to /BR and no other devices are requesting, /BR stops being asserted immediately.

10. /BBSY also takes one time delay after BG3 to assert

11. /BBSY is active, so after one delay BG1 stops being asserted

12. BG1 isn’t active, so after one delay BG2 stops being asserted

13. BG2 isn’t active, so after one delay BG3 stops being asserted